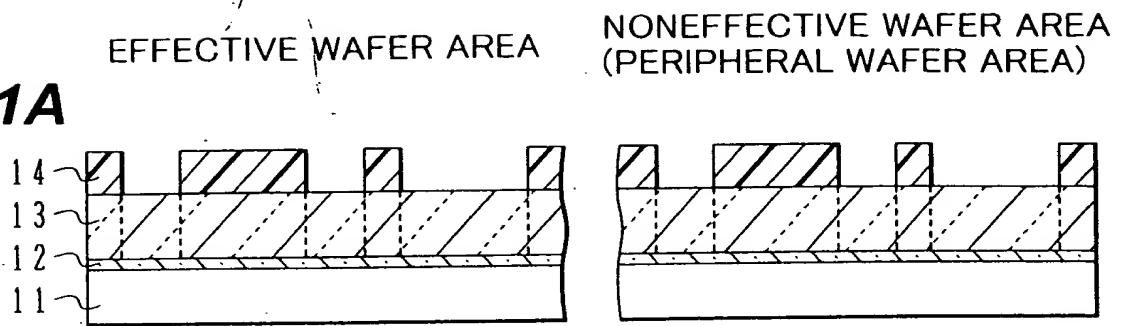
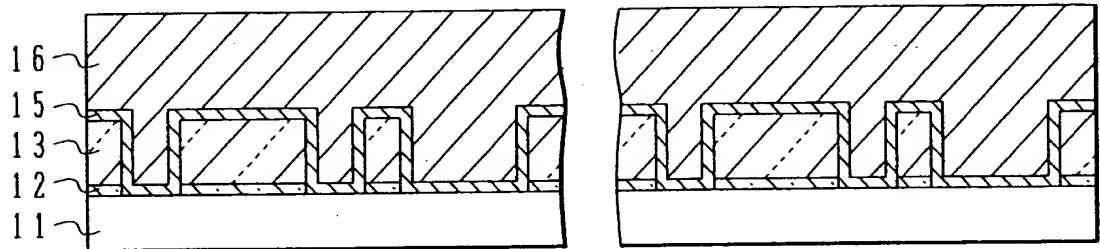


00937013-111304  
FIG. 1A

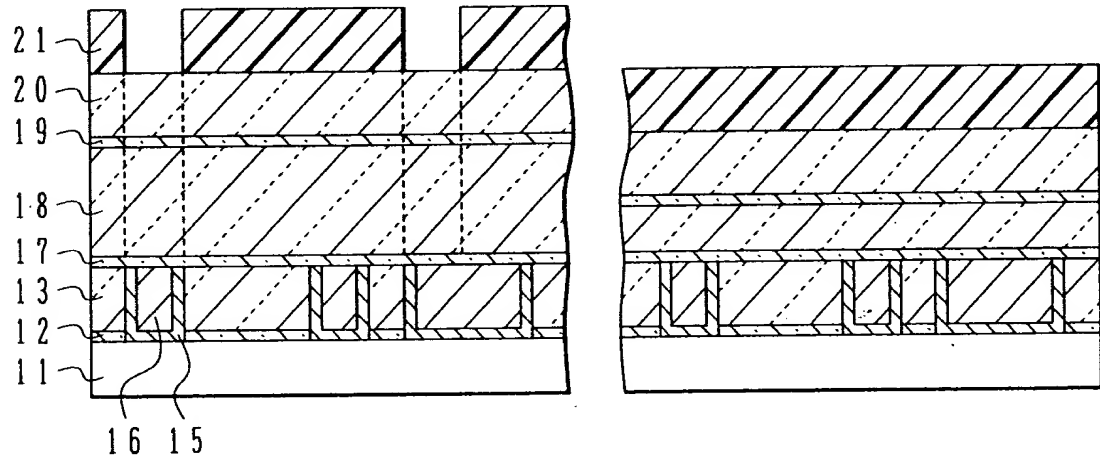
**FIG. 1A**



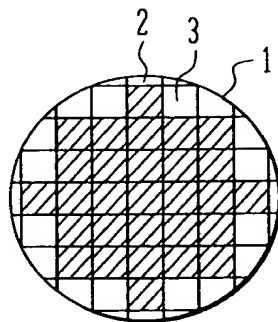
**FIG. 1B**



**FIG. 1C**

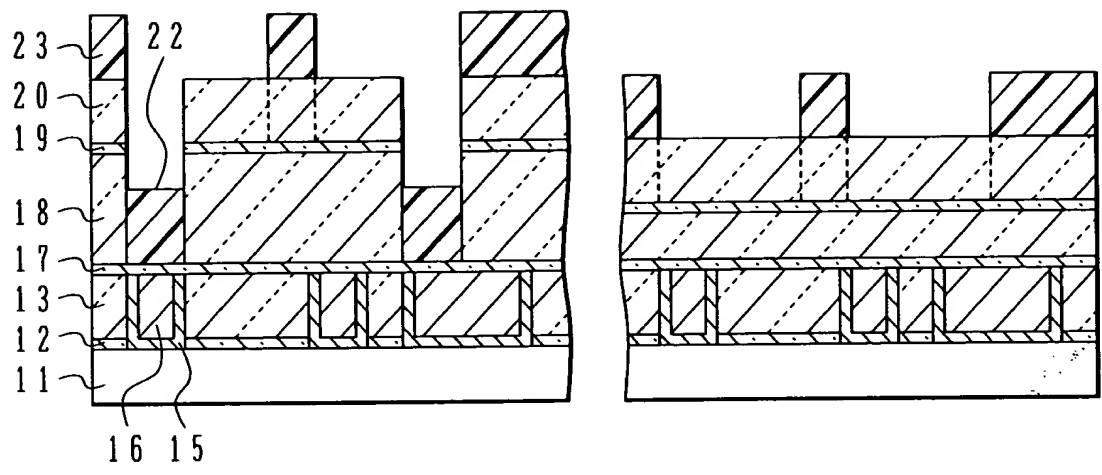


**FIG. 1D**

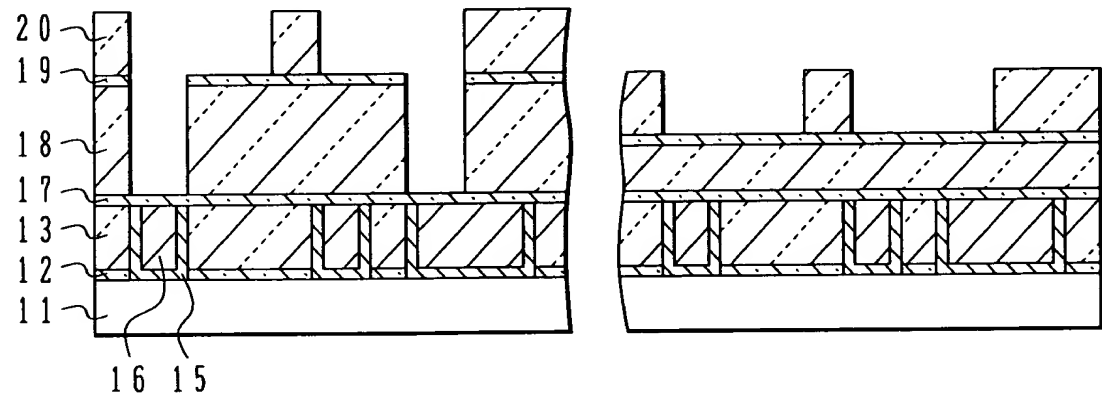


09367043.44301

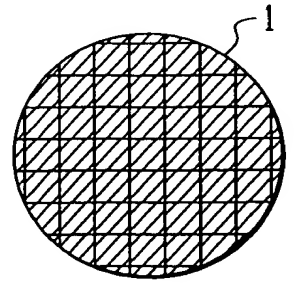
**FIG.1E**



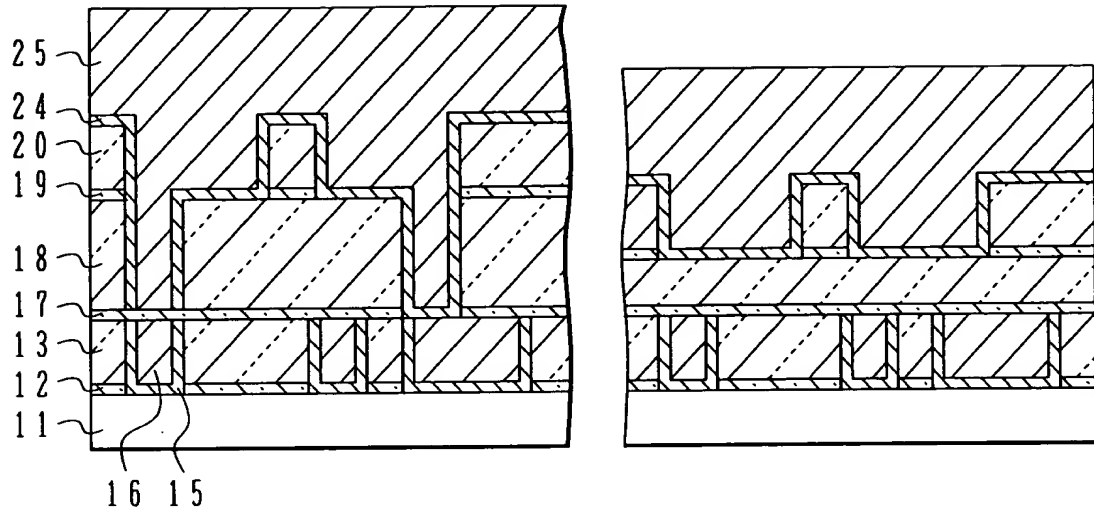
**FIG.1F**



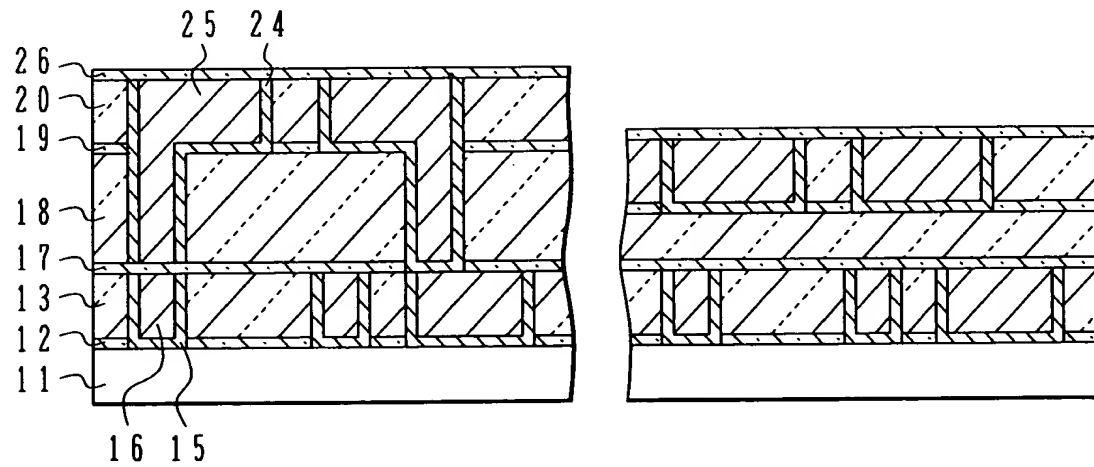
**FIG.1G**



**FIG.1H**

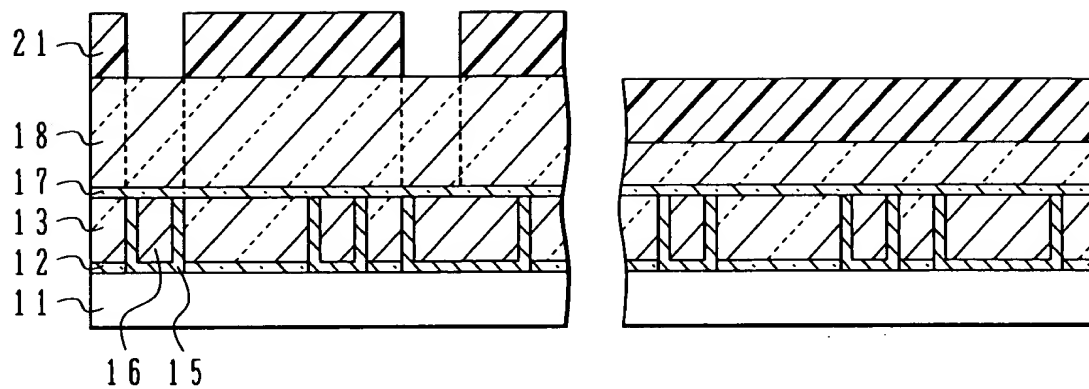


**FIG.1I**

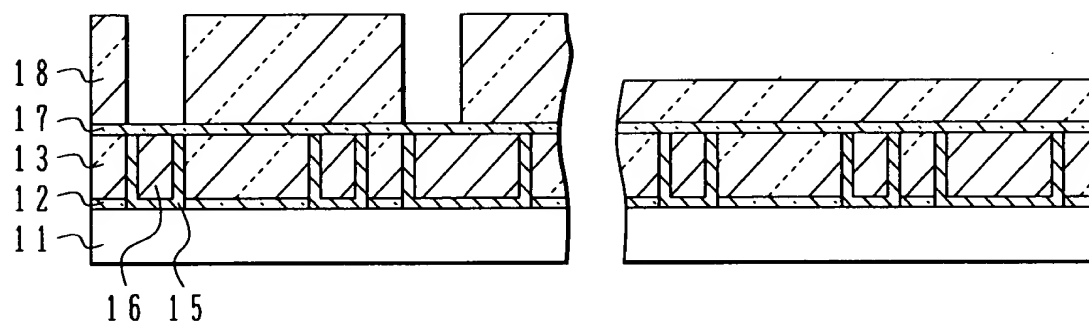


09987042-111301

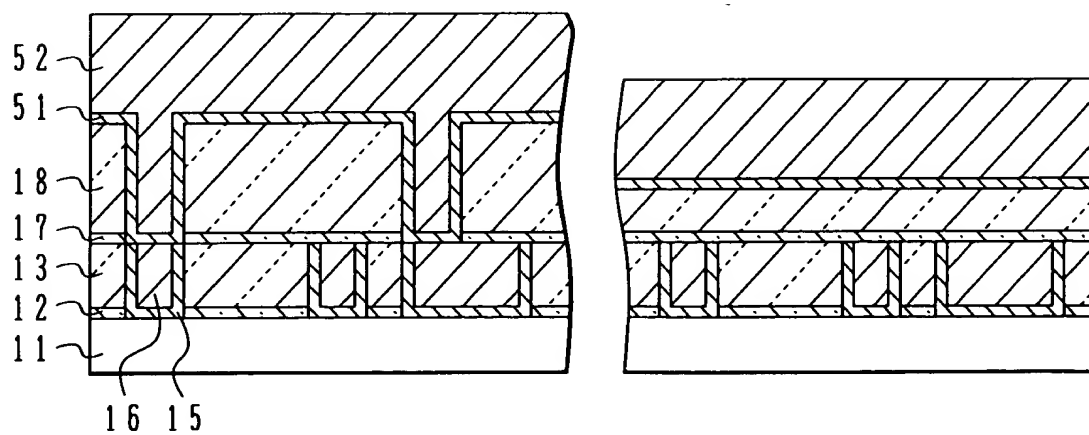
**FIG.2A**



**FIG.2B**

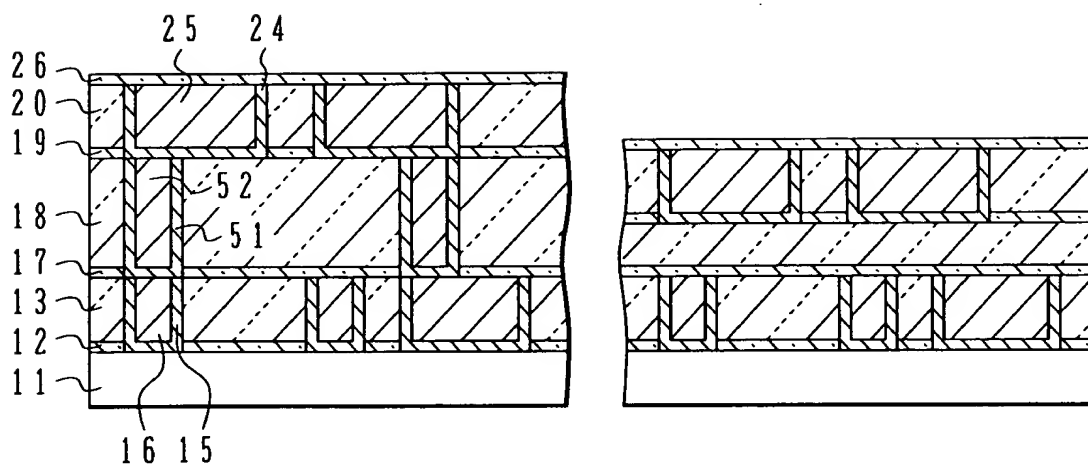


**FIG.2C**





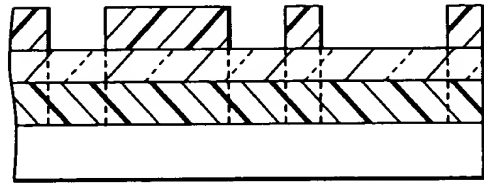
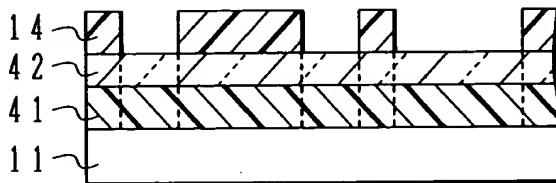
**FIG.2G**



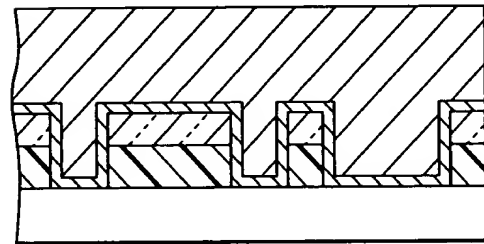
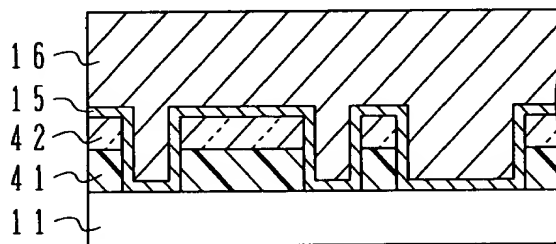
EFFECTIVE WAFER AREA

NONEFFECTIVE WAFER AREA  
(PERIPHERAL WAFER AREA)

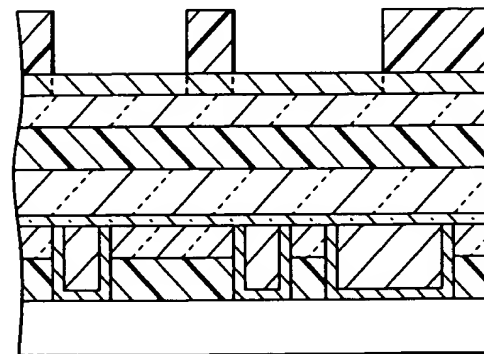
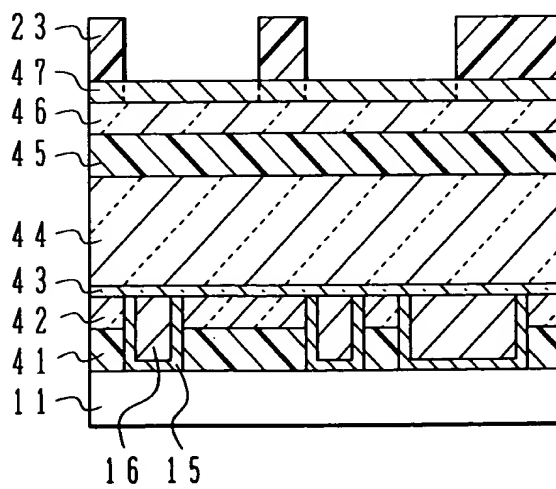
**FIG.3A**



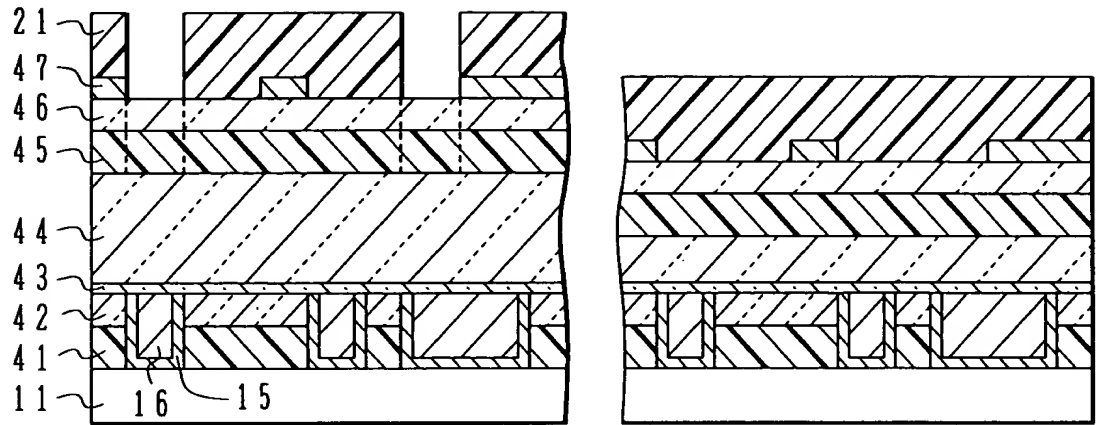
**FIG.3B**



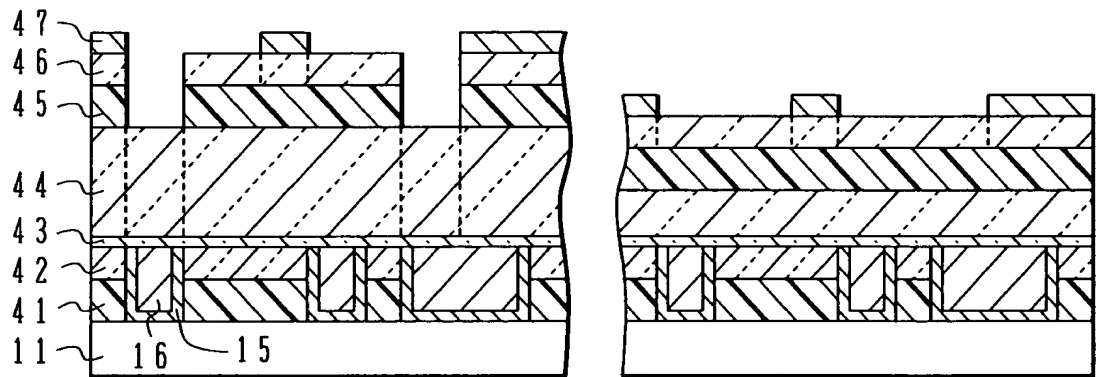
**FIG.3C**



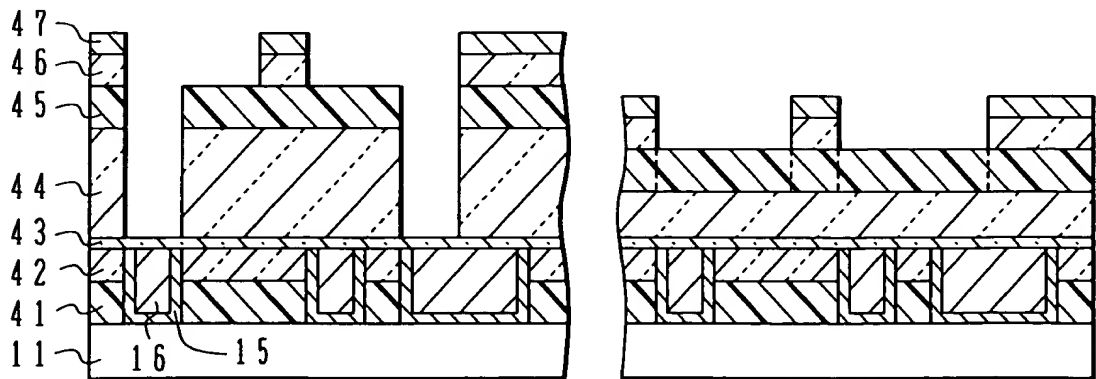
**FIG.3D**



**FIG.3E**

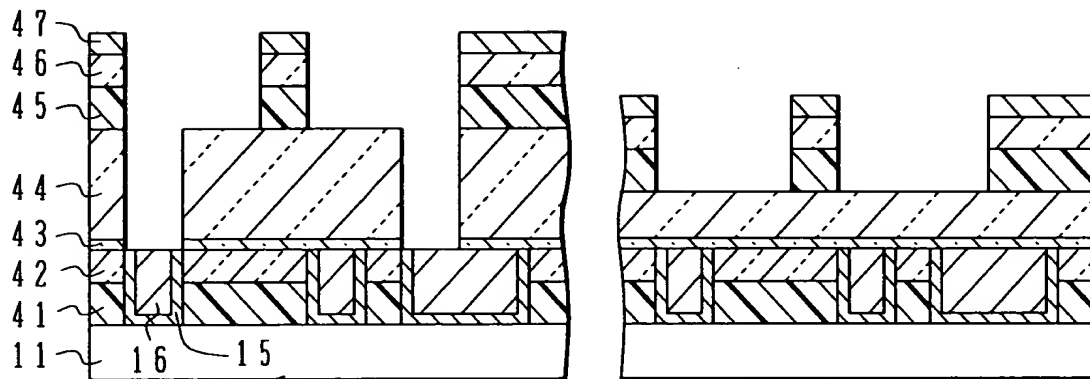


**FIG.3F**

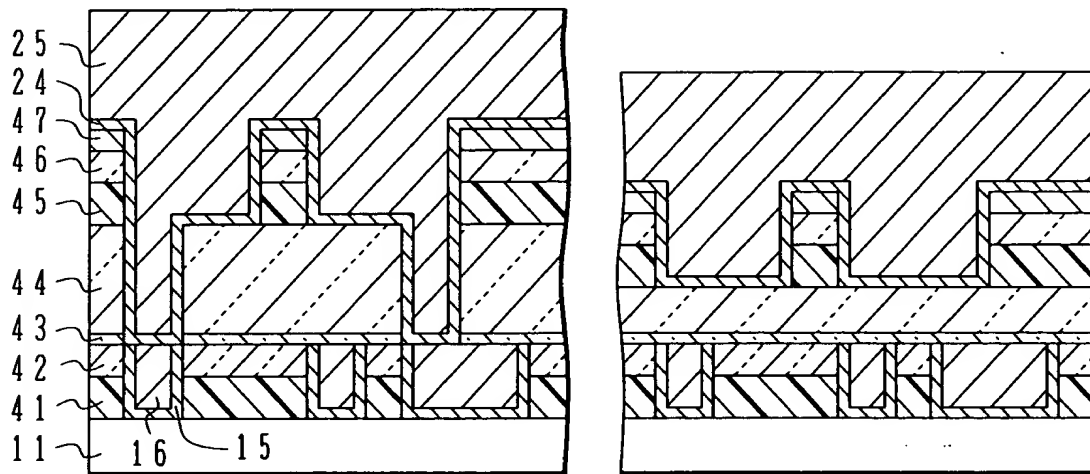




**FIG.3G**



**FIG.3H**



**FIG.3I**

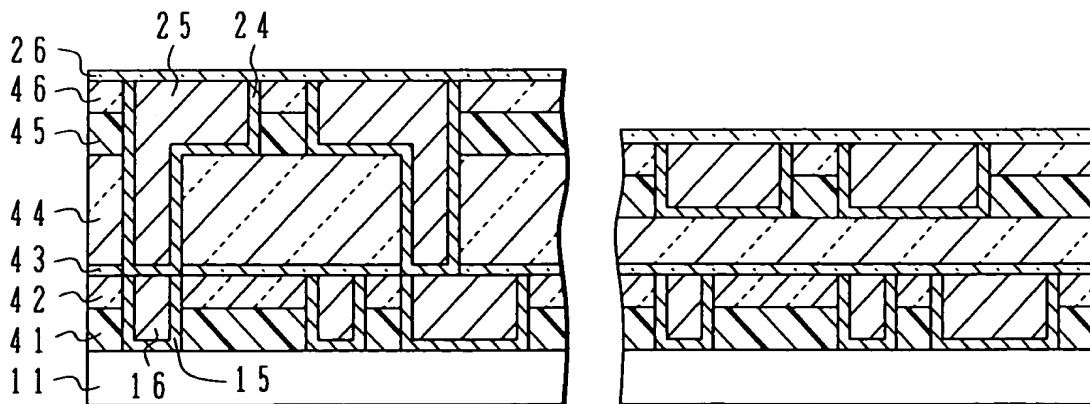
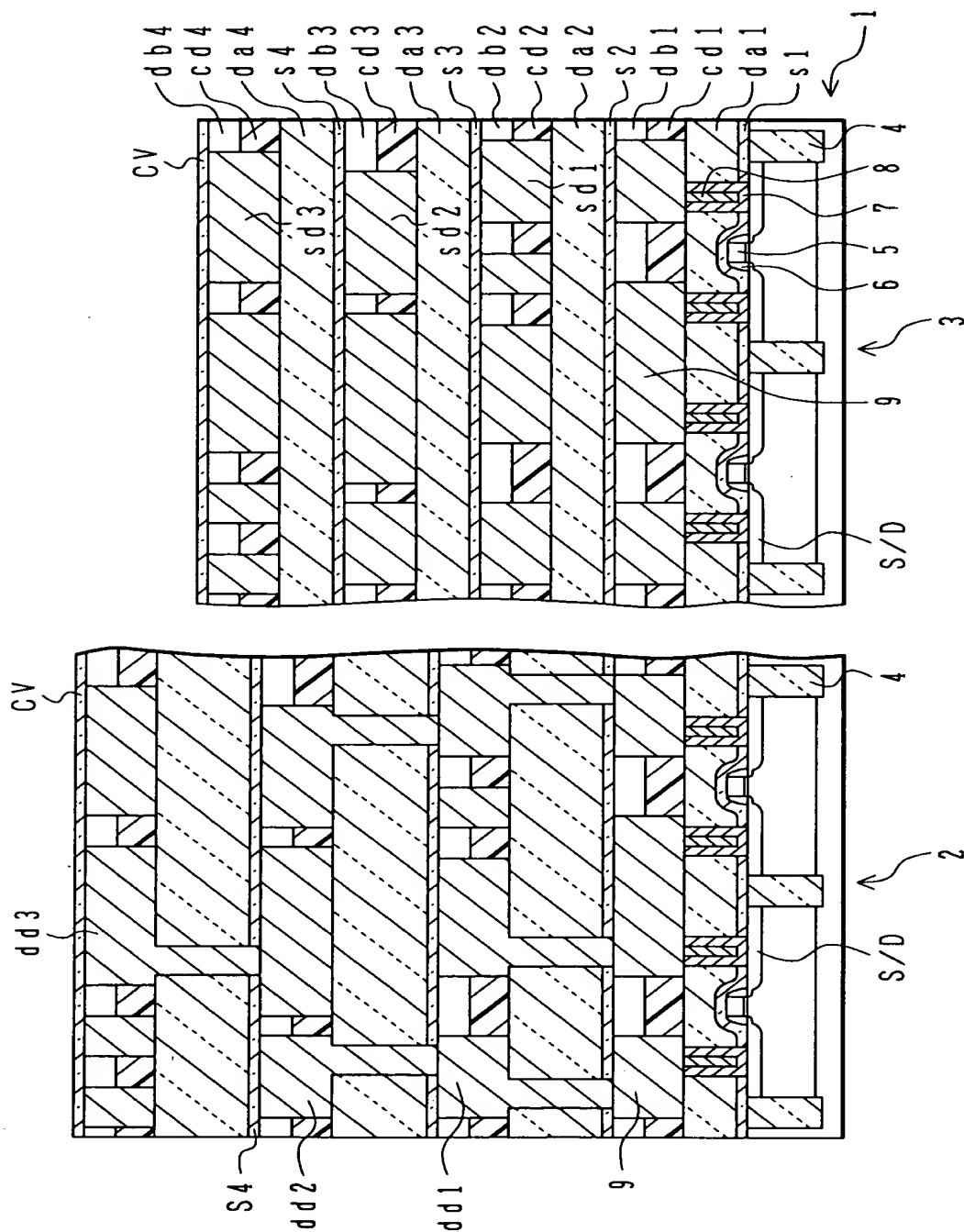


FIG. 4

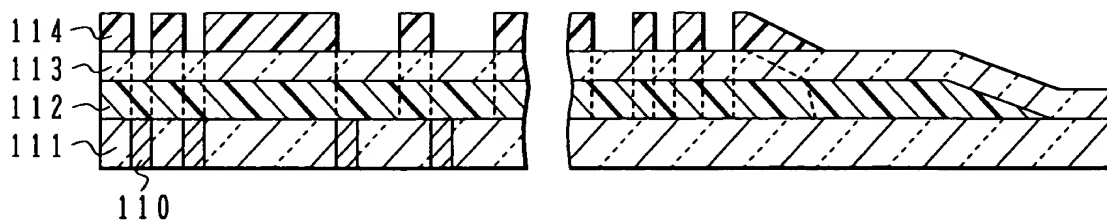


09987041 21078660

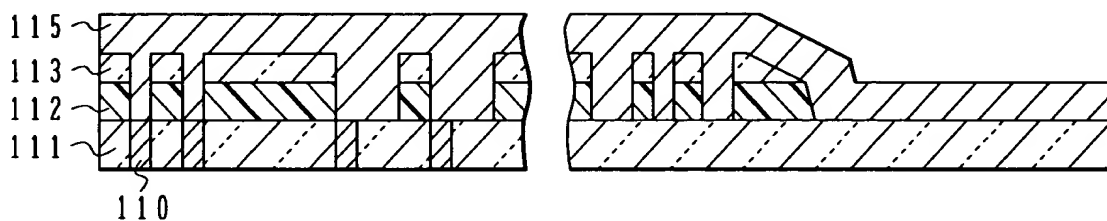
EFFECTIVE WAFER AREA

NONEFFECTIVE WAFER AREA  
(PERIPHERAL WAFER AREA)

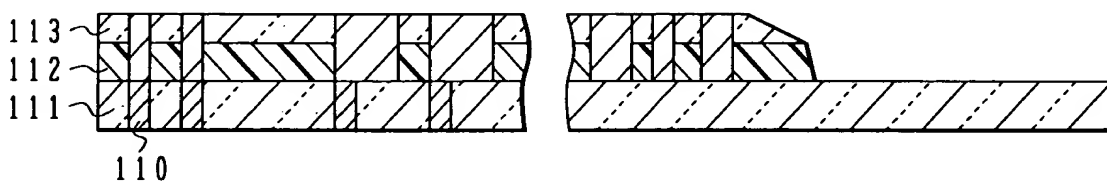
**FIG.5A**



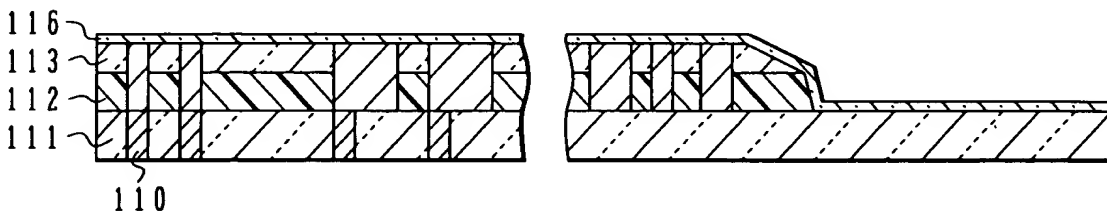
**FIG.5B**



**FIG.5C**



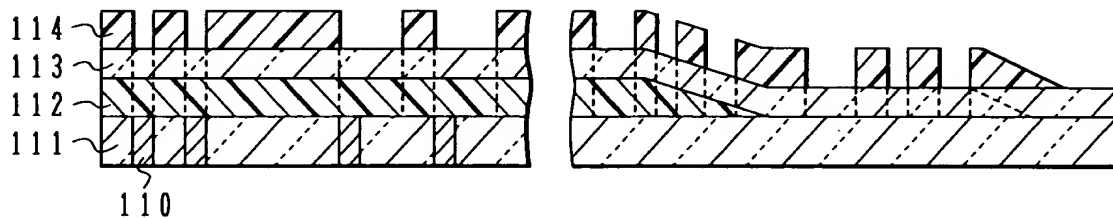
**FIG.5D**



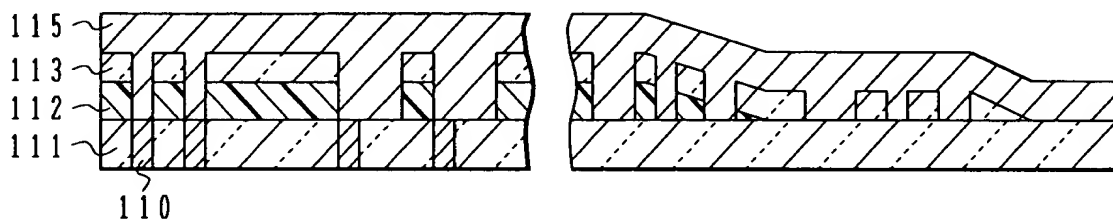
EFFECTIVE WAFER AREA

NONEFFECTIVE WAFER AREA  
(PERIPHERAL WAFER AREA)

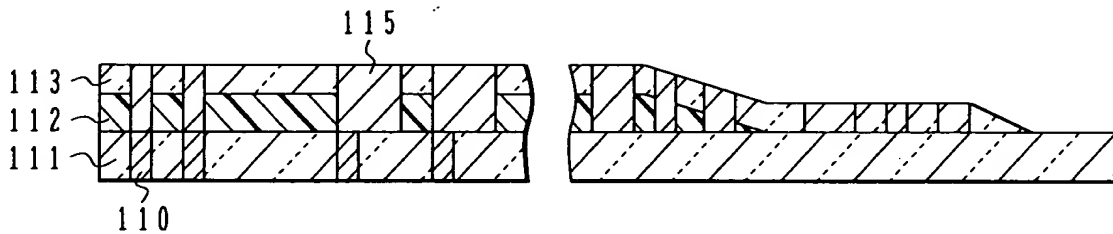
**FIG.6A**



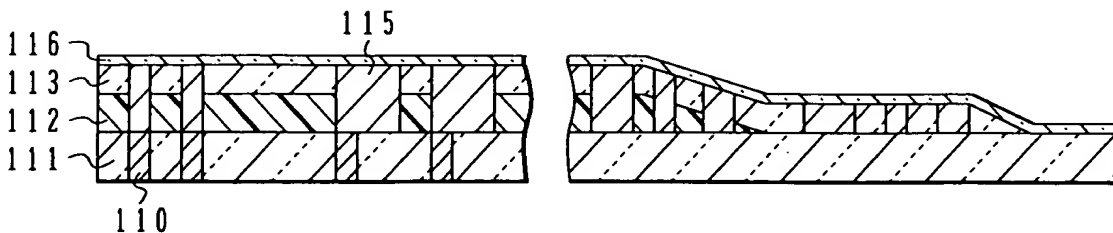
**FIG.6B**



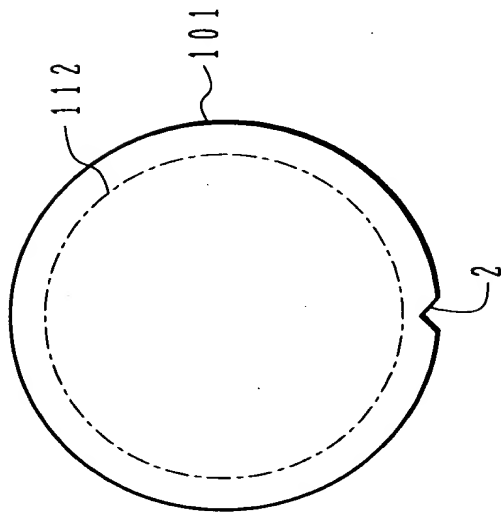
**FIG.6C**



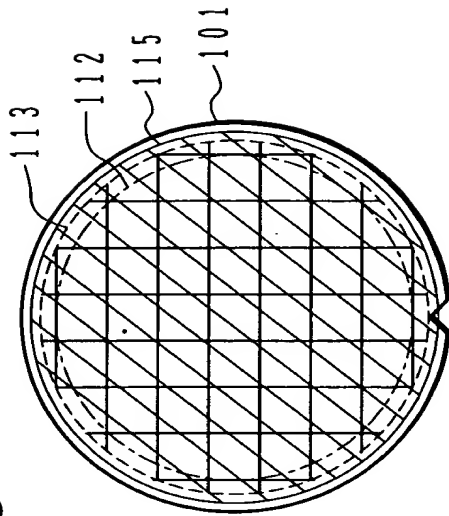
**FIG.6D**



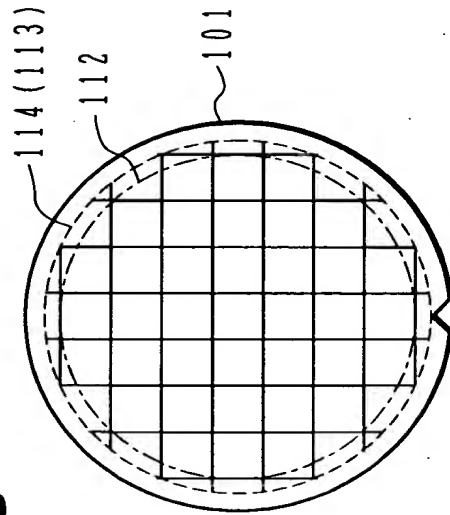
**FIG.7A**



**FIG.7C**



**FIG.7B**



**FIG.7D**

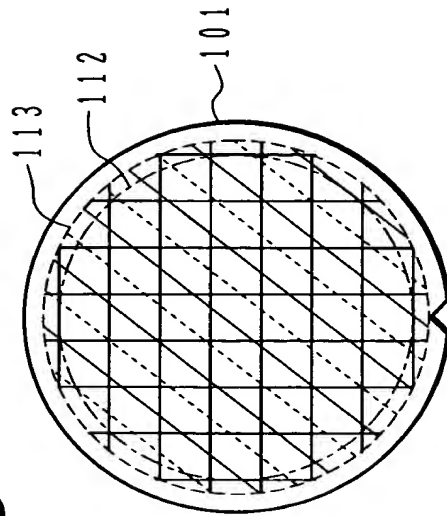


FIG. 8A

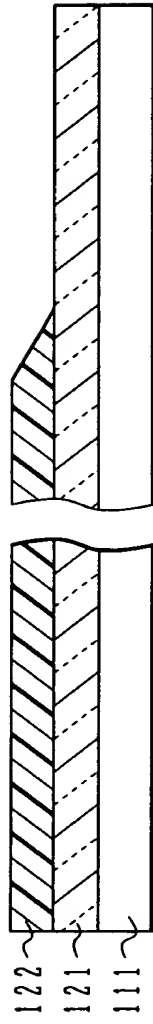
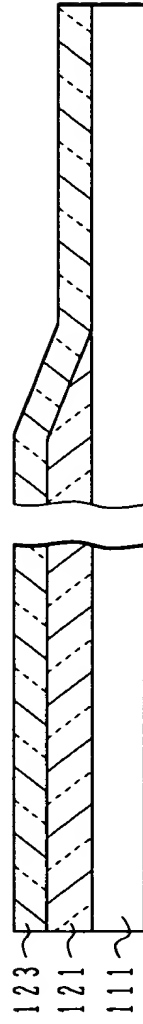


FIG. 8B



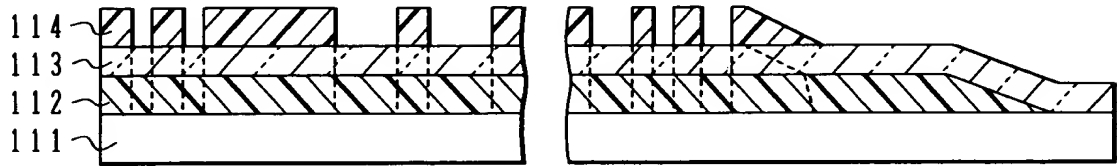
FIG. 8C



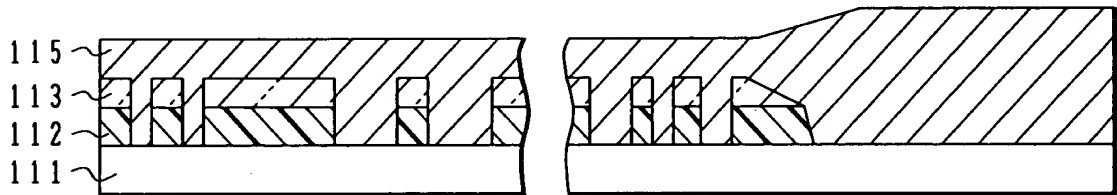
EFFECTIVE WAFER AREA

NONEFFECTIVE WAFER AREA  
(PERIPHERAL WAFER AREA)

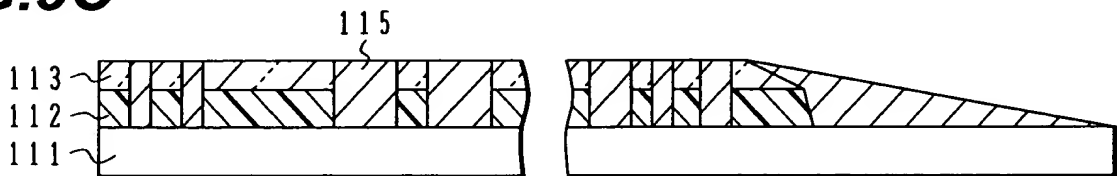
**FIG.9A**



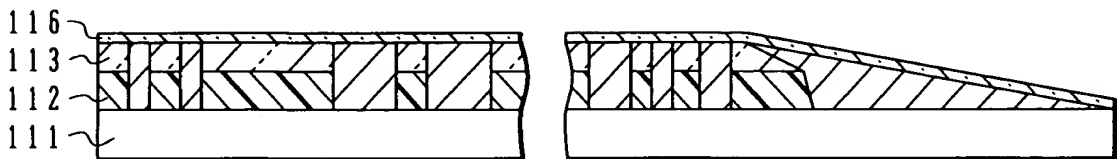
**FIG.9B**



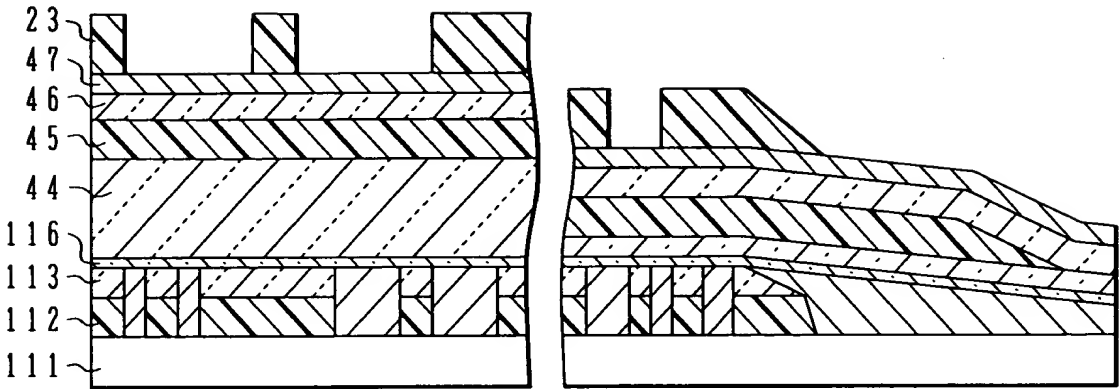
**FIG.9C**



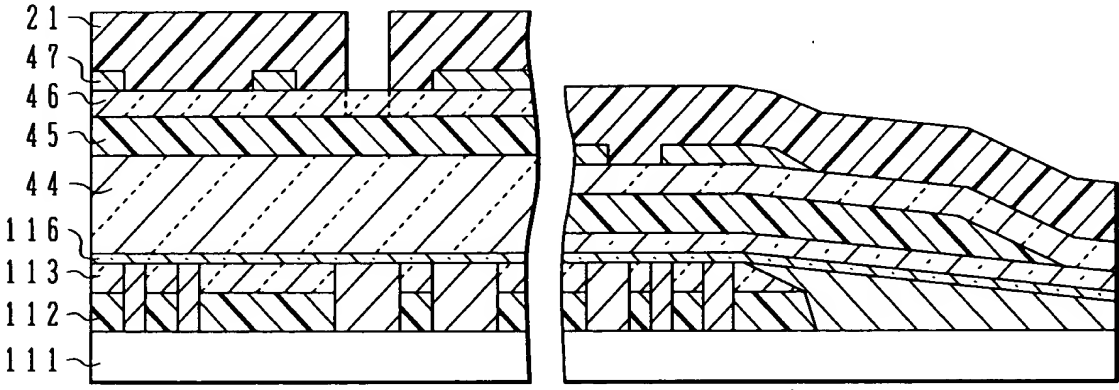
**FIG.9D**



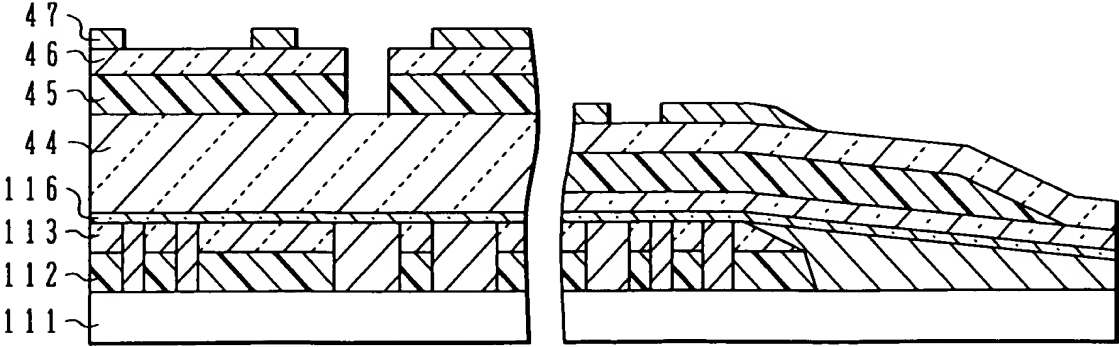
**FIG.9E**



**FIG.9F**

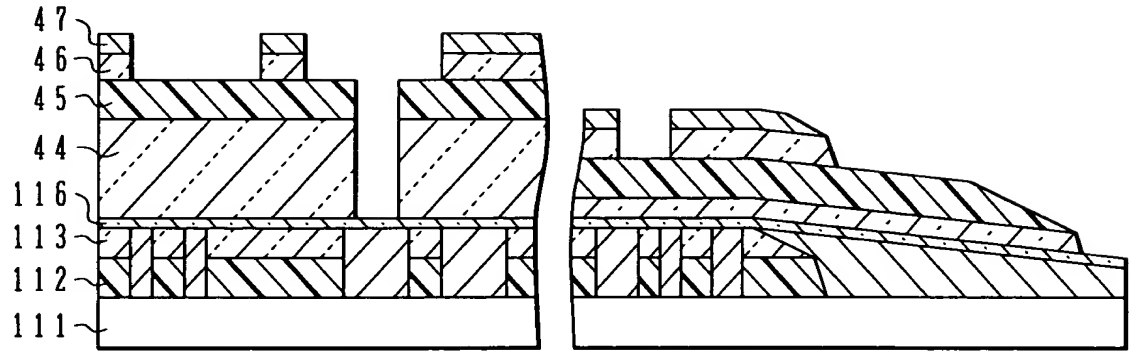


**FIG.9G**

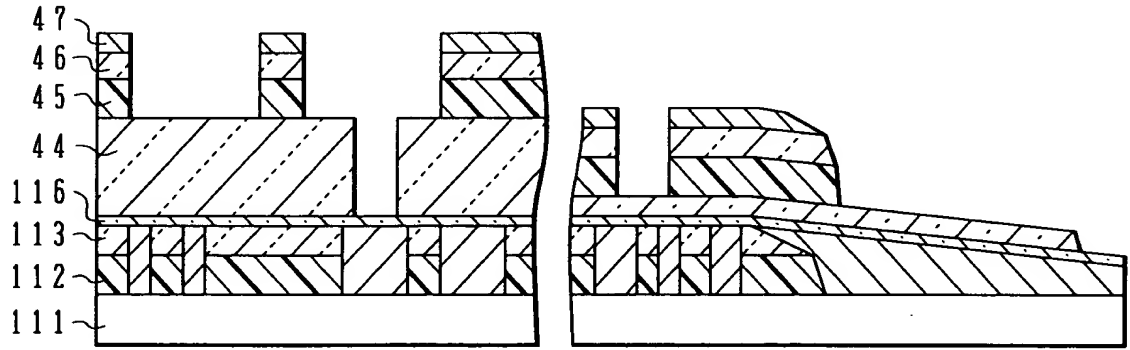




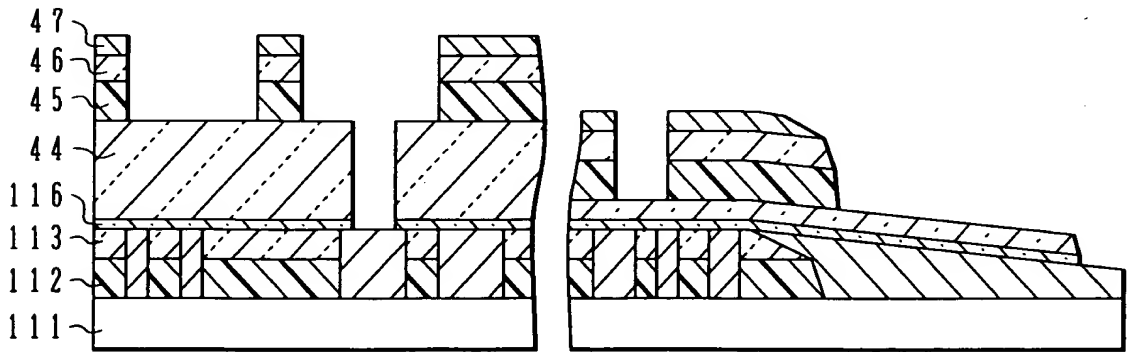
**FIG.9H**



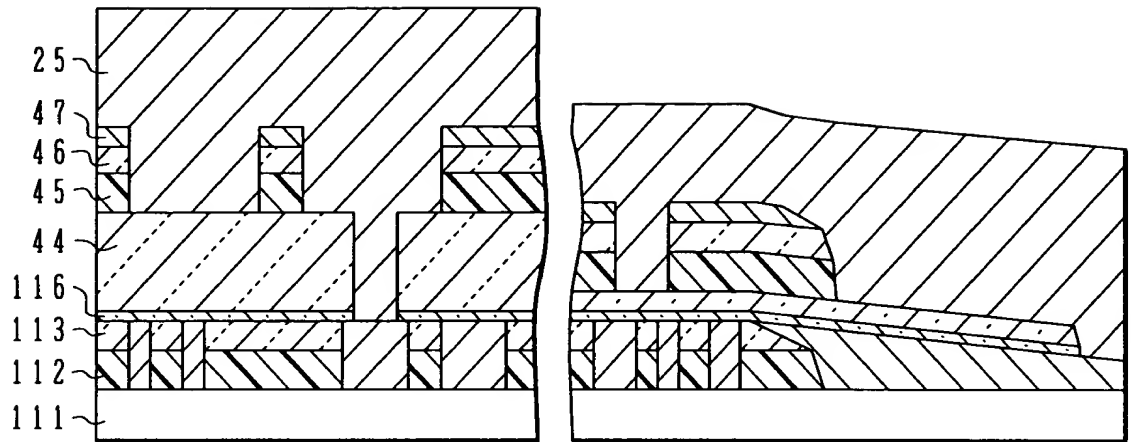
**FIG.9I**



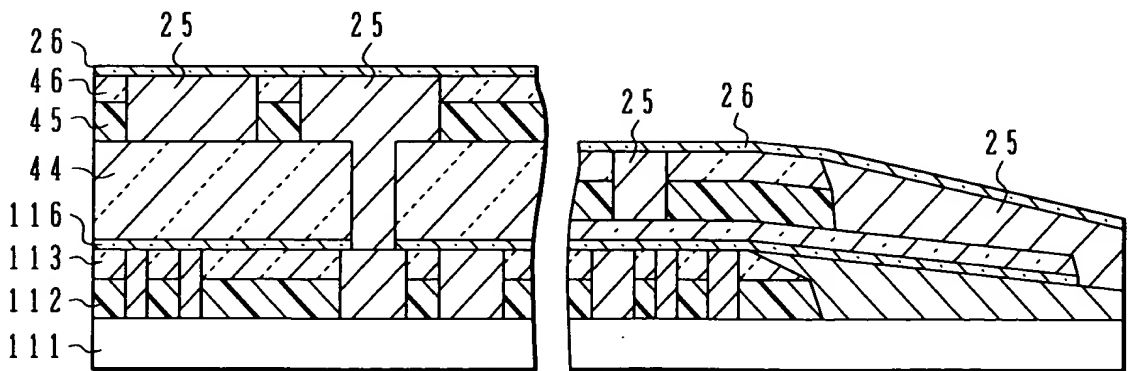
**FIG.9J**

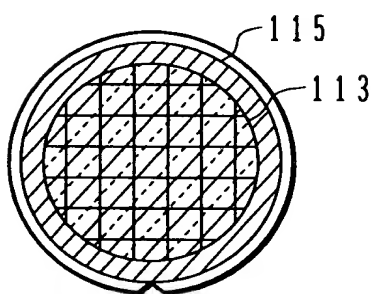


**FIG.9K**

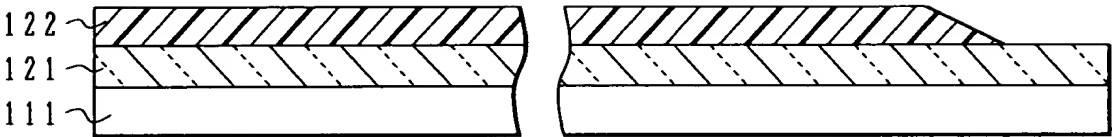


**FIG.9L**

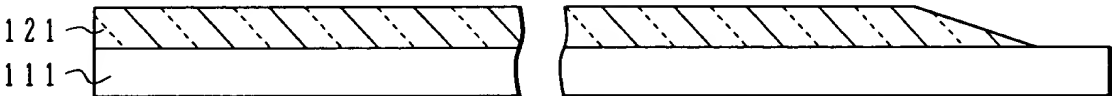




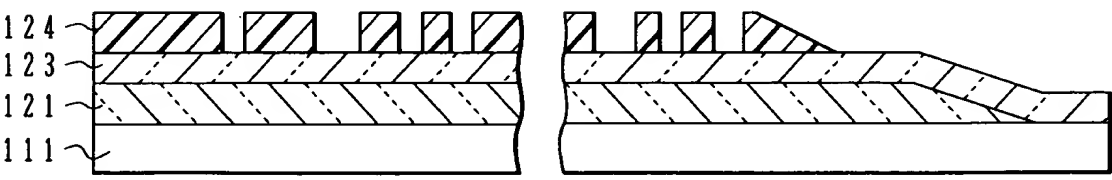
**FIG. 11A**



**FIG. 11B**



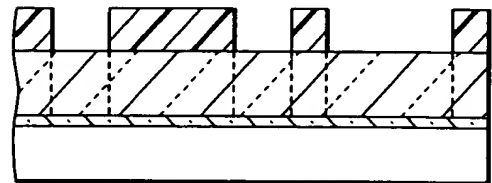
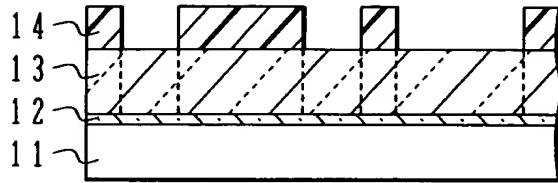
**FIG. 11C**



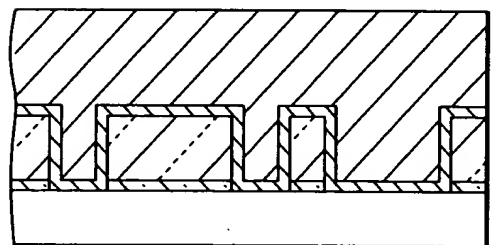
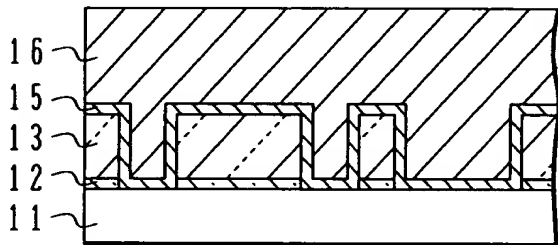
EFFECTIVE WAFER AREA

NONEFFECTIVE WAFER AREA  
(PERIPHERAL WAFER AREA)

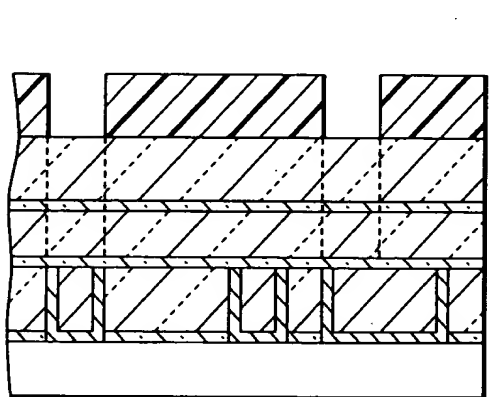
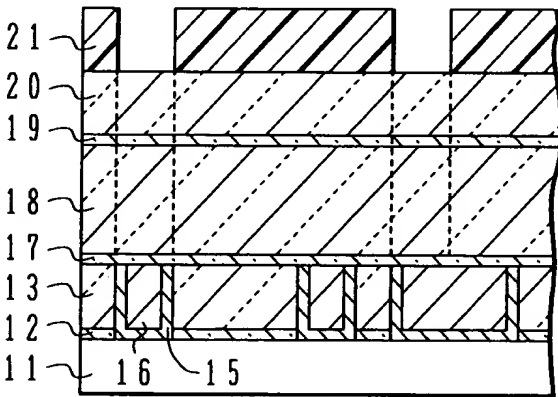
**FIG.12A**



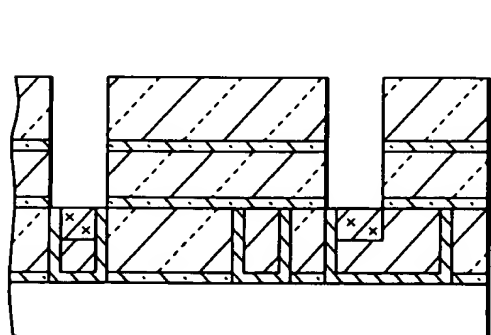
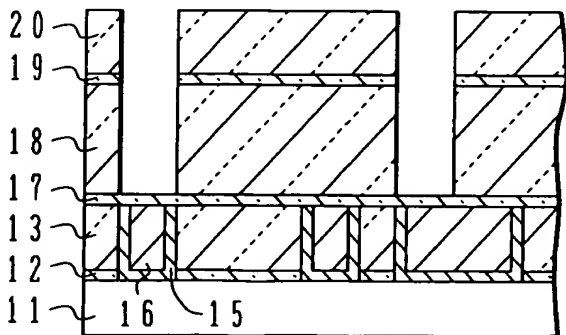
**FIG.12B**



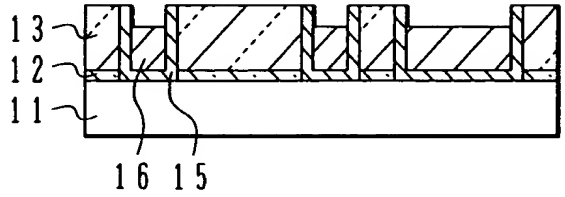
**FIG.12C**



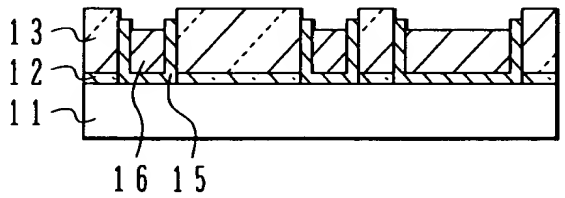
**FIG.12D**



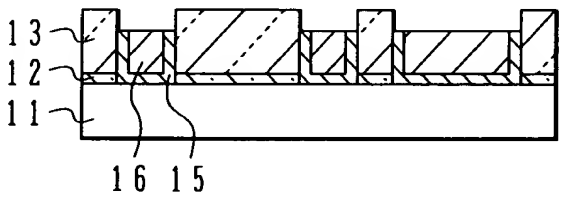
**FIG.13A**



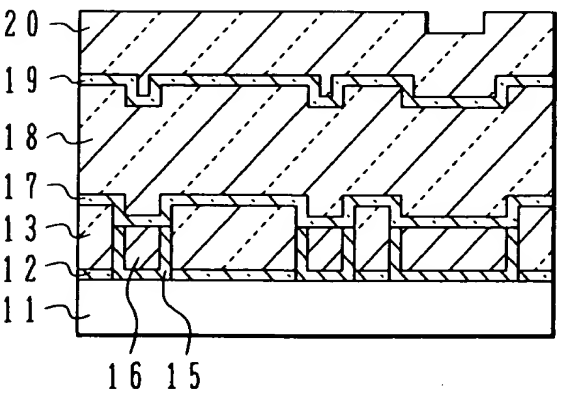
**FIG.13B**



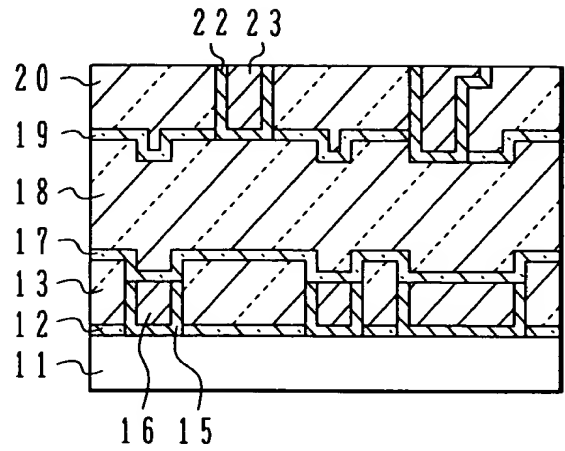
**FIG.13C**



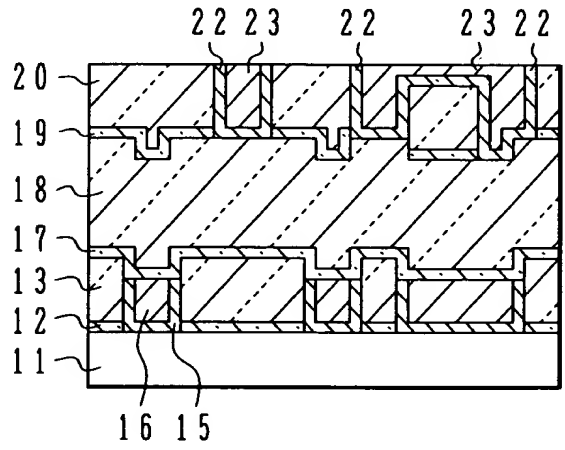
**FIG.13D**



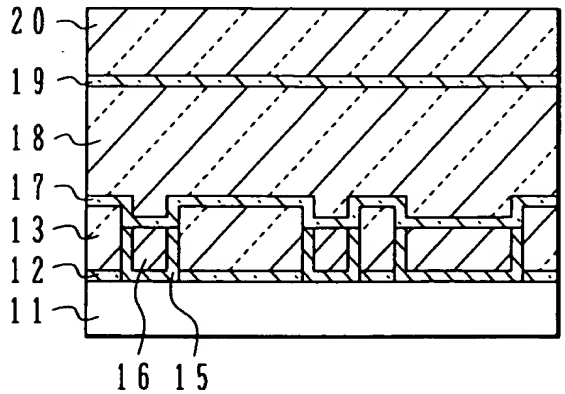
**FIG.13E**



**FIG.13F**



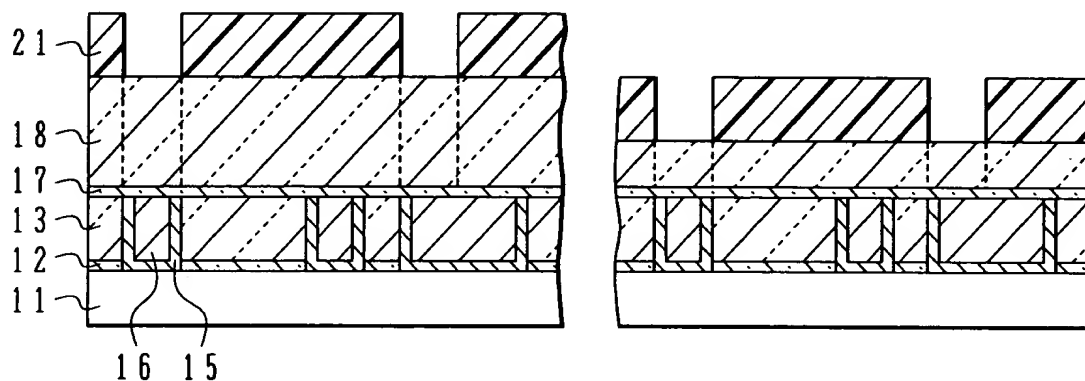
**FIG.13G**



EFFECTIVE WAFER AREA

NONEFFECTIVE WAFER AREA  
(PERIPHERAL WAFER AREA)

**FIG.14A**



**FIG.14B**

